DATA SHEET



MOS FIELD EFFECT TRANSISTOR 2SK3324

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

The 2SK3324 is N-Channel MOS FET device that features a Low gate charge and excellent switching characteristics, and Designed for high voltage applications such as switching power supply, AC adapter.

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3324	TO-3P

FEATURES

- Low gate charge : $Q_G = 32 \text{ nC TYP.} (V_{DD} = 450 \text{ V}, \text{V}_{GS} = 10 \text{ V}, \text{I}_{D} = 6.0 \text{ A})$
- Gate voltage rating : ±30 V
- Low on-state resistance :
- RDS(on) = 2.8 Ω MAX. (VGS = 10 V, ID = 3.0 A)
- Avalanche capability ratings

ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage	Vdss	900	V
Gate to Source Voltage	VGSS(AC)	±30	V
Drain Current (DC)	D(DC)	±6	А
Drain Current (Pulse) ^{Note1}	D(pulse)	±18	А
Total Power Dissipation (Tc = 25°C)	P⊤	120	W
Total Power Dissipation (TA = 25°C)	P⊤	3.0	W
Storage Temperature	Tstg	–55 to + 150	°C
Single Avalanche Current Note2	AS	6.0	А
Single Avalanche Energy Note2	Eas	21.6	mJ

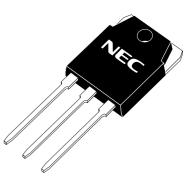
Notes 1. PW \leq 10 μ s, Duty cycle \leq 1 %

2. Starting T_{ch} = 25 °C, V_{DD} = 150 V, R_G = 25 Ω , V_{GS} = 20 V \rightarrow 0 V

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Document No. D14203EJ2V0DS00 (2nd edition) Date Published January 2000 NS CP(K) Printed in Japan The mark \star shows major revised points.

(TO-3P)



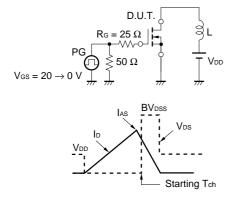
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ELECTRICAL CHARACTERISTICS (TA = 25 °C)

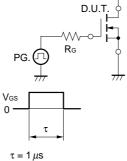
CHARACTERISTICS	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS	
Drain Leakage Current	loss			100	μA	Vds = 900 V, Vgs = 0 V	
Gate to Source Leakage Current	lgss			±100	nA	$V_{GS} = \pm 30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$	
Gate to Source Cut-off Voltage	V _{GS(off)}	2.5		3.5	V	V _{DS} = 10 V, I _D = 1.0 mA	
Forward Transfer Admittance	y₁s	2.5	3.3		S	Vds = 20 V, Id = 3.0 A	
Drain to Source On-state Resistance	RDS(on)		2.5	2.8	Ω	Vgs = 10 V, Id = 3.0 A	
Input Capacitance	Ciss		1000		pF	Vds = 10 V,	
Output Capacitance	Coss		200		pF	V _{GS} = 0 V, f = 1 MHz	
Reverse Transfer Capacitance	Crss		42		pF		
Turn-on Delay Time	td(on)		17		ns	Vdd = 150 V,	
Rise Time	tr		38		ns	I _D = 3.0 A, V _{GS(on)} = 10 V,	
Turn-off Delay Time	td(off)		57		ns		
Fall Time	tr		33		ns	$R_G = 10 \Omega, R_L = 10 \Omega$	
Total Gate Charge	QG		32		nC	V _{DD} = 450 V, V _{GS} = 10 V,	
Gate to Source Charge	QGS		5		nC		
Gate to Drain Charge	Qgd		20		nC	ID = 6.0 A	
Body Diode Forward Voltage	VF(S-D)		0.9		V	IF = 6.0 A, VGS = 0 V	
Reverse Recovery Time	trr		1.9		μs	IF = 6.0 A, VGS = 0 V,	
Reverse Recovery Charge	Qrr		9.0		μC	di/dt = 50 A/µs	

TEST CIRCUIT 1 AVALANCHE CAPABILITY

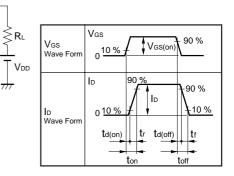
TEST CIRCUIT 2 SWITCHING TIME



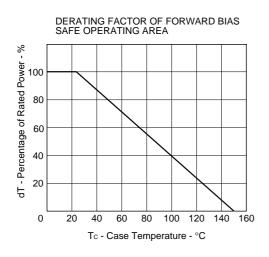
TEST CIRCUIT 3 GATE CHARGE



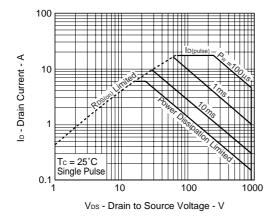
Duty Cycle $\leq 1 \%$



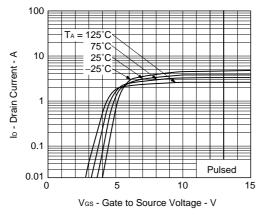
TYPICAL CHARACTERISTICS (TA = 25 °C)

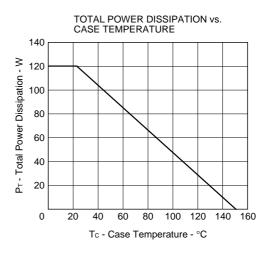




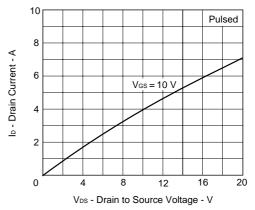


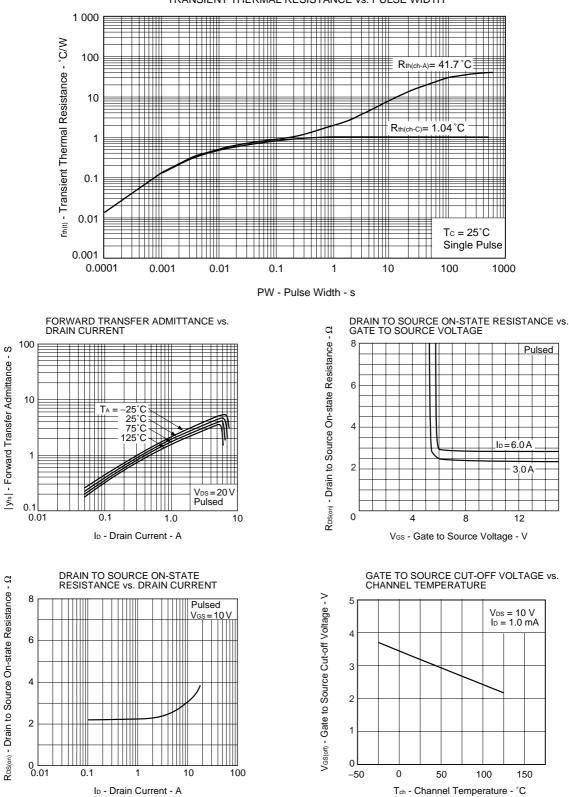




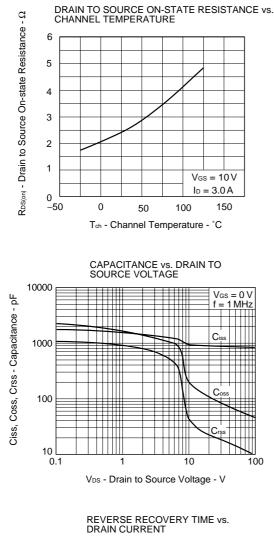




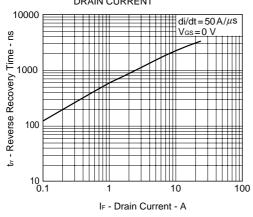


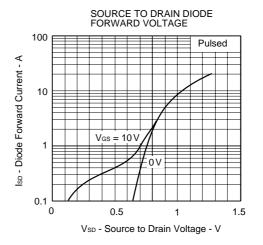


TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

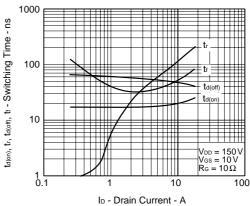


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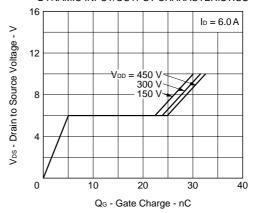


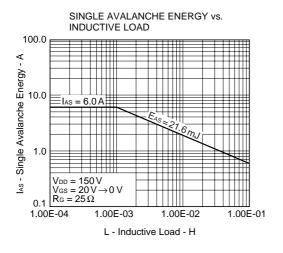


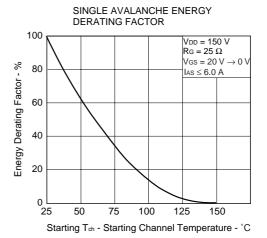






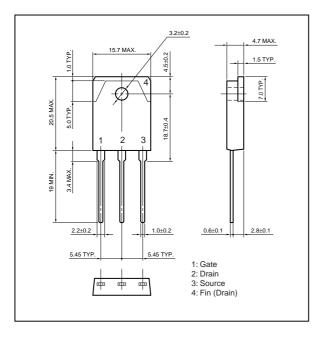




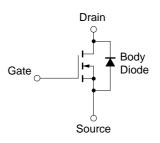


PACKAGE DRAWING (Unit : mm)

TO-3P (MP-88)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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